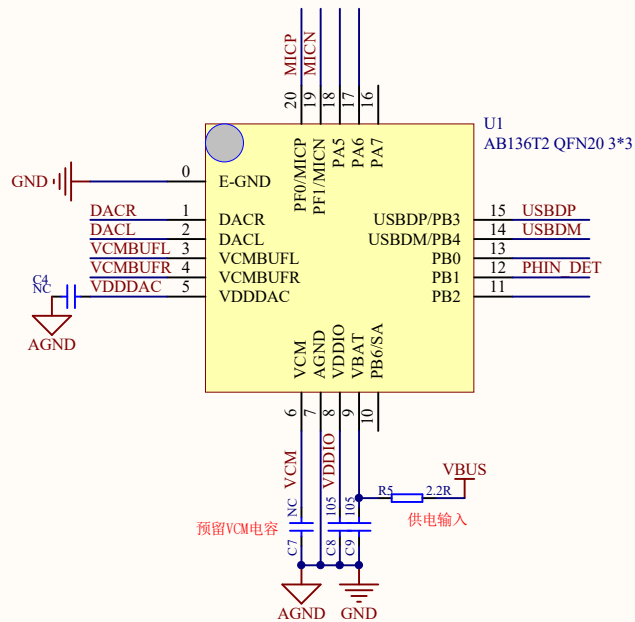
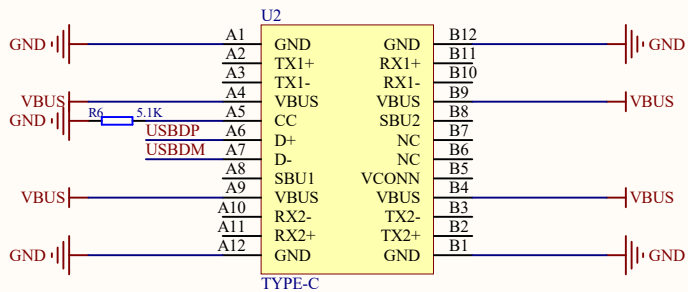


PIN FUNCTION			
	ADC	USB	update
PA5			
PA6			
PA7	ADC0		
PB0			
PB1	ADC1		
PB2	ADC2		
PB3	USBDP	TX0-G4/HSTRX-G7	
PB4	USBDM		
PB6	ADC3		
PFO/MICP	ADC4		
PFL1/MICN			
DACL	ADC7		
DACR	ADC8		
VCMBUFL	ADC9		
VCMBUFR	ADC10		
VBAT	ADC14		

定义:  
 Left -> DACL  
 Right -> DACR  
 MIC OR GND (PJ3) -> VCMBUFL / PFO\_MICP  
 GND OR MIC (PJ4) -> VCMBUFR / PFL1\_MICN



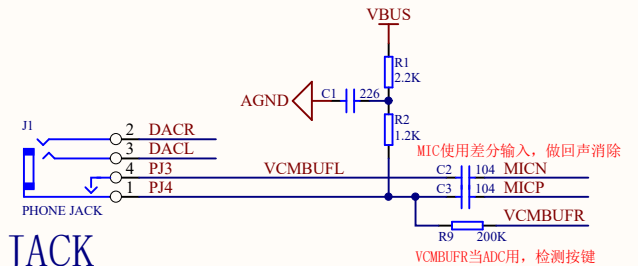
## AB136T2 QFN20 TYPE-C转接头OMTP V0.2



## TYPE-C Connector

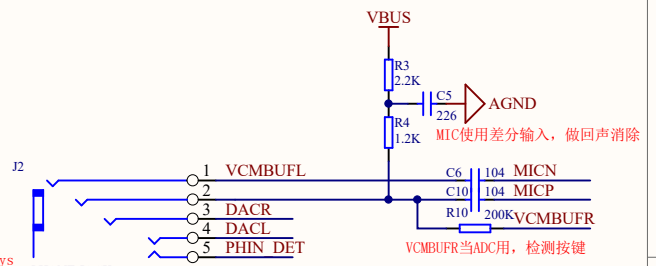
2 Right (PJ2)  
 3 Left (PJ1)  
 4 GND (PJ3)  
 1 MIC (PJ4)

## 4pin PHONE JACK



1 GND (PJ4)  
 2 MIC (PJ3)  
 3 Right (PJ2)  
 4 Left (PJ1)  
 5 Switch Open-always

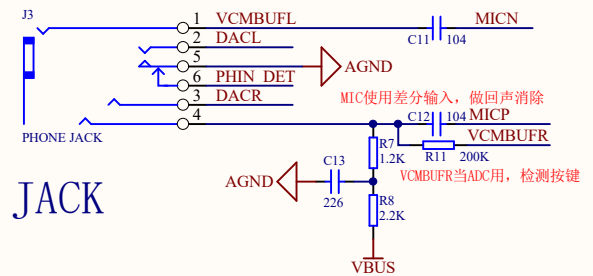
## 5pin PHONE JACK



PHIN\_DET开内部上拉200K到VDDIO 3.3V  
 DACL处于开启工作状态, 1.3V  
 PHIN\_DET使用ADC检测, 1.3V为插入状态, 3.3V为拔出状态

1 GND (PJ4)  
 2 Left (PJ1)  
 5 insulation  
 6 5-6 Open-always  
 3 Right (PJ2)  
 4 MIC (PJ3)

## 6pin PHONE JACK



pin5/pin6 独立  
 PCB把PIN5接地, PHIN\_DET开上拉检测  
 插入会被拉低状态  
 拔出为高状态